Appin No. 09/575,110 Amdt. Dated October 7, 2003 Reply to Office action of July 7, 2003

2



Amendments to the Specification:

The paragraph beginning at Page 1, lines 21-30, to be amended as follows:

USSN	Our Docket	Our Title
	Number	
TBA09/575,152	MJ62US	Fluidic seal for an ink jet nozzle assembly
TBA6,428,133	1J52US	Ink jet print head having a moving nozzle with an externally arranged actuator
TBA6,526,658	UM52US	Method of manufacture of an ink jet print head having a moving nozzle with an externally arranged actuator
TBA6.328.417	MJ63US	Ink jet print head nozzle array
TBA6,390,591	MJ58US	Nozzle guard for an ink jet print head

The paragraph beginning at Page 2, lines 9-30, through to Page 2, lines 1-3, to be amended as follows:

-Various methods, systems and apparatus relating to the present invention are disclosed in the following co-pending applications filed by the applicant or assignee of the present invention on 23 May 2000:

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09/575.197 (NPA001US),	09/575,195 (NPA002US),	NPA003US,
09/575,159 (NPA004US),	09/575.132 (NPA005US),	09/575,123-(NPA006US),
09/575,148-(NPA007US),	09/575,130 (NPA008US),	09/575.165 (NPA009US),
09/575,153-(NPA010US),	09/575,118 (NPA012US),	09/575,131 (NPA016US),
09/575.116 (NPA017US)	09/575,144 (NPA018US),	09/575,139 (NPA019US),
09/575,186 (NPA020US),	09/575,185 (NPA021US),	09/575,191-(NPA030US),
09/575,145-(NPA035US),	09/575,192-(NPA048US),	09/609,303 (NPA050US),
09/610,095 (NPA051US),	09/609.596 (NPA052US)	09/575,181 (NPA075US),
<u>09/575,193 (NPB001US),</u>	09/575,156 (NPB002US),	09/575,183 (NPK002US),
<u>09/575,160 (NPK003US)</u> ,	09/575.150 (NPK004US),	09/575,169 (NPK005US),
(NPK007US),	09/575,184 (NPM001US),	6,502,614 (NPM002US),
<u>09/575,180 (NPM003US),</u>	09/575.149 (NPM004US),	6,549,935 (NPN001US),
(NPN002US),	(NPN003US),	-09/575,187 (NPP001US),
(NPP002US),	09/575.155 (NPP003US),	6.591.884 (NPP005US),
6,439,706 (NPP006US),	09/575,196 (NPP007US),	09/575,198 (NPP008US),

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Appln No. 09/575,110 Amdt. Dated October 7, 2003 Reply to Office action of July 7, 2003

3

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<u>09/575178-(NPP016US)</u> ,	<u>6.428.155-(NPP017US),</u>	09/575,146 (NPP018US),
<u>09/608,920 (NPP019US),</u>	09/575,174 (NPS001US),	09/575.163 (NPS003US),
<u>09/575.168 (NPS020US)</u> ,	09/575,154 (NPT001US),	09/575,129 (NPT002US),
<u>09/575,124 (NPT003US),</u>	09/575.188 (NPT004US),	09/575.189 (NPX001US),
09/575,162-(NPX003US),	09/575.172 (NPX008US),	09/575.170 (NPX011US),
09/575,171-(NPX014US),	09/575.161-(NPX016US),	(NPX020US),
10/291,716 (NPX022US),	6,428,133 (1J52US),	• • •
6.315.399 (MJ10US),	6,338,548 (MJ11US),	6.527.365 (IJM52US),
	•	<u>6.540,319 (MJ12US),</u>
<u>6.328,431 (MJ13US),</u>	<u>6,328.425 (MJ14US),</u>	<u>09/575,127 (MJ15US)</u> ,
<u>6.383,833 (MJ34US)</u> ,	6.464,332-(MJ47US),	MJ52US,
6.390,591 (MJ58US),	09/575.152 (MJ62US),	6.328,417-(MJ63US),
6.409.323 (PAK04US),	6.281.912 (PAK05US),	6.604,810 (PAK06US),
09/575.112 (PAK07US),	6.488.422 (PAK08US),	09/575,108 (PECOTUS),
09/575,109 (PEC02US).		22/2/2/100 (1 De0100);

The disclosures of these co-pending applications are incorporated herein by reference.—

The paragraph beginning at Page 7, lines 26-29, through to Page 2, lines 1-2, to be amended as follows:

USSN	Our Docket	Our Title
	Number	
TBA09/517,539	AUTH01	Validation Protocol and System
09/112,7636,566,	AUTH02	Circuit for Protecting Chips Against IDD
<u>858</u>		Fluctuation Attacks
09/112,737	AUTH04	Method for Protecting On-Chip Memory (Flash
		and RAM)
09/112,761	AUTH05	Method for Making a Chip Tamper-Resistant
09/113,2236,442,	AUTH06	A system for authenticating physical objects
<u>525</u>		
TBA09/517,384	AUTH07	Validation Protocol and System
TBA09/505,951	AUTH08	Validation Protocol and System
09/505,003<u>6,</u>374,	AUTH09	Consumable Authentication Protocol and
<u>354</u>		System

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Appin No. 09/575,110 Amdt. Dated October 7, 2003 Reply to Office action of July 7, 2003



09/517,608	AUTH10	Consumable Authentication Protocol and System
09/505,147	AUTH11	Consumable Authentication Protocol and System
09/505,952	AUTH12	Unauthorized Modification of Values Stored in Flash Memory
TBA09/517,380	AUTH13	A System for the Manipulation of Secure Data
09/516,874	AUTH14	An Authentication Chip with Protection from Power Supply Attacks
TBA <u>09/517,541</u>	AUTH15	Shielding Manipulations of Secret Data